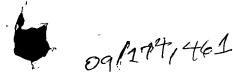
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ABSTRACT OF THE DISCLOSURE

A right and left camera for picking up images, an image combining circuit for generating a 3-D image by combining right and left images, a 3-D image display for displaying the 3-D image output from the image combining circuit, a display memory for use in displaying the right and left images on the 3-D image monitor, and a recording memory for use in recording the right and left images are shared and double buffer memories are used to perform write and read operations concurrently in alternating manner, allowing the right and left images to be combined and displayed as a 3-D image on the 3-D image display at a high data rate.

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